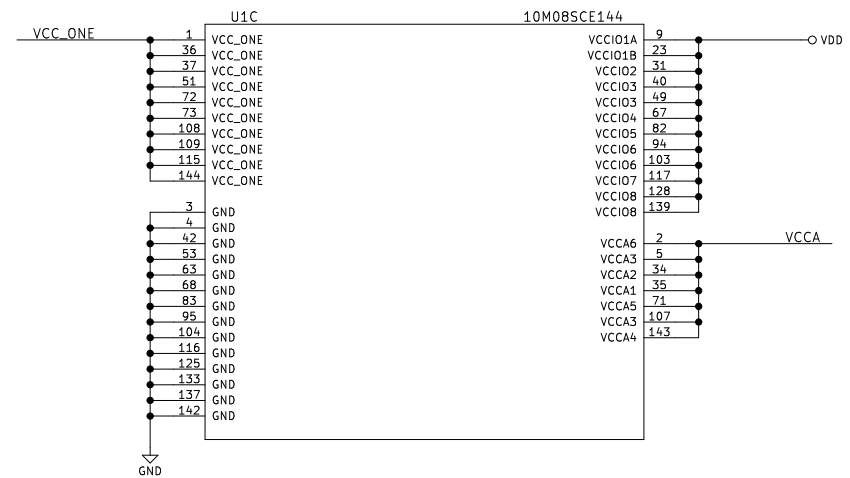
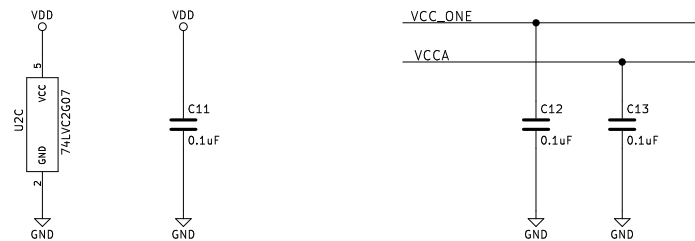




TITLE		REV
FPGA Module (MAX10)		
DATE 2019-08-09	DOCUMENT NUMBER S1MOD0001-SCH	SHEET 2
DESIGNED BY T.Matsuda	SIMGICS Simple Logic Sysytems LTD.	2